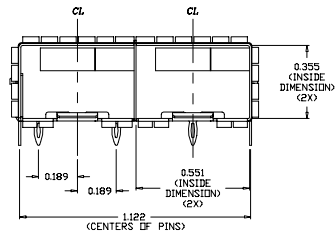
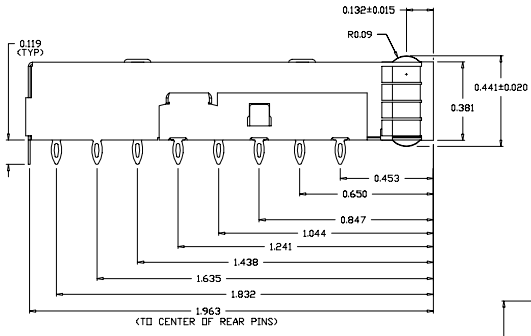
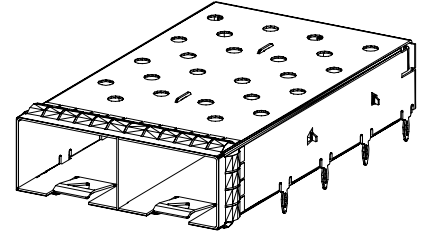
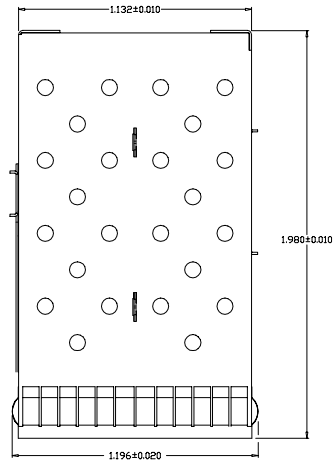
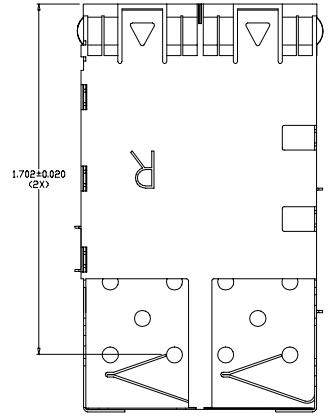


REVISION AND CHANGE EFFECTIVITY DATE				
LTR.	ECN	DESCRIPTION	DATE	APP'D.

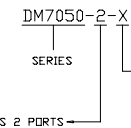


NOTES:

- 1) OPERATING AND STORAGE TEMPERATURE: -40°C TO +160°C
- 2) CAGE MATERIAL: 0.010" THICK BRASS ALLOY; C2680 - FULL HARD
- 3) PLATING: SEE PLATING OPTIONS
- 4) CAGE IS SOLDERED AT SEVERAL LOCATIONS
- 5) CAGE IS STAMPED "R" FOR ROHS COMPLIANCE
- 6) GASKET: 0.002" THICK BERYLLIUM COPPER WITH NICKEL PLATING
- 7) GASKET IS SPOT WELDED INTO CAGE



ORDERING INFO:



PLATING OPTIONS:
(ALL ARE ROHS COMPLIANT)

- R FOR 100µIN MATTE TIN OVER 50µIN NICKEL NOT INTENDED FOR REFLOW WAVE SOLDER ONLY - WAVE TEMP. 260°C FOR 6 SEC. MAX
- G FOR FLASH GOLD OVER 50µIN NICKEL REFLOW COMPATIBLE - MAX REFLOW TEMP. 260°C
- N FOR 100µIN NICKEL REFLOW COMPATIBLE - MAX REFLOW TEMP. 260°C

DO NOT SCALE DRAWING.

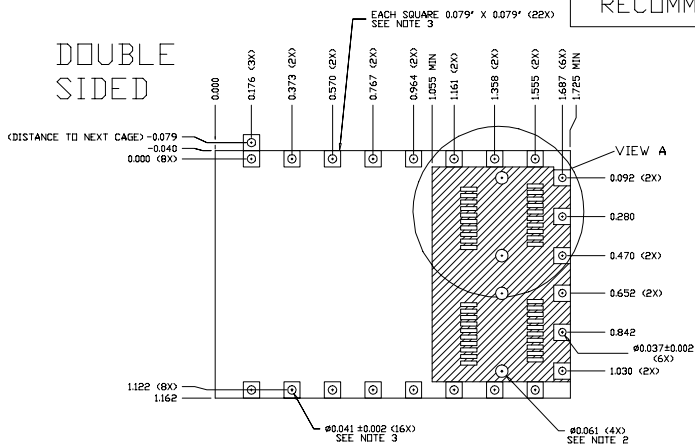
EXPERIMENTAL NO:		DIVISION ASSIGNED: dataMate Division	
TOLERANCE UNLESS OTHERWISE SPECIFIED METRIC INCHES ± FRACS. DEC. XX ± .010 XXX ± .005 ANGLES ± TOOLING DWG □ PART DWG □ BREAK SHARP EDGES REMOVE ALL BURRS		MATERIAL: SEE NOTES FINISH: SEE NOTES DRAWN BY: A. PIRILLIS CHECKED BY: J. NOWAK ENGR. APPROVAL: B. SKEPNEK APPROVED BY: A. CHIAPPETTA DATE: 10/02/07 DATE: 10/02/07 DATE: 10/02/07 DATE: 10/02/07	
THE INFORMATION DISCLOSED IN THIS DOCUMENT IS PROPRIETARY TO METHODE ELECTRONICS, INC. AND MAY NOT BE USED FOR MANUFACTURE OR ANY OTHER PURPOSE WITHOUT THE WRITTEN CONSENT OF METHODE. CHANGES SUBJECT TO CHANGE AS THEY MAY OCCUR WITH RESPECT TO PRODUCT IMPROVEMENT.		TITLE: 2 PORT SFP+ EMI CAGE (IMPROVED) WITH GASKET PRESS FIT PINS SIZE: C CODE IDENT.: DWG. NUMBER: DM7050-2-X Rev.: SCALE: SHEET: 1 OF 2	



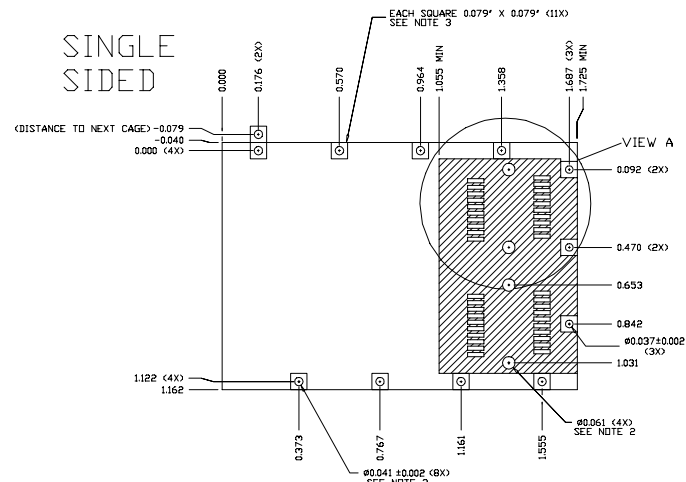
RECOMMENDED FOOTPRINT

REVISION AND CHANGE EFFECTIVITY DATE				
LTR.	ECN	DESCRIPTION	DATE	APP'D.

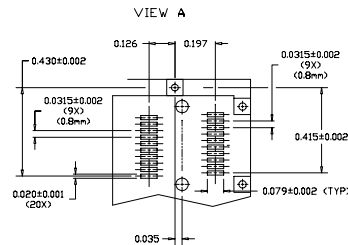
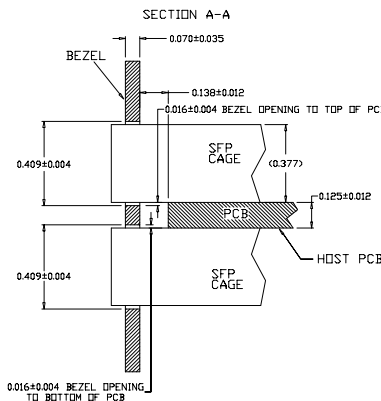
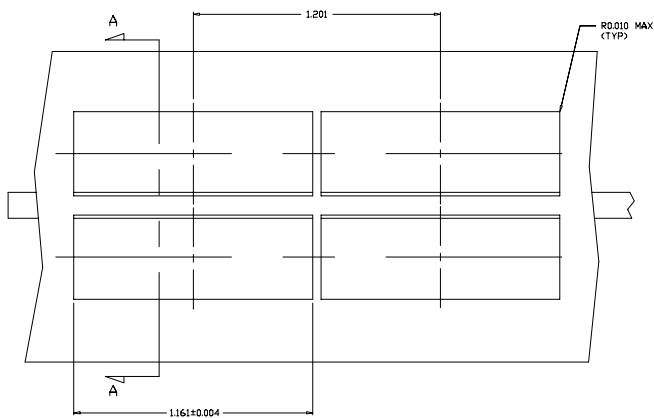
DOUBLE SIDED



SINGLE SIDED



RECOMMENDED PANEL CUTOUT



NOTES:

- CUTOUTS ARE TO HAVE R0.010 MAX RADII
- THRU HOLES, PLATING OPTIONAL
- PADS AND VIAS ARE CHASSIS GROUND

4) TRACES ONLY
NO COMPONENTS

4) NO TRACES
NO COMPONENTS
(EXCEPT CHASSIS GROUND)

DO NOT SCALE DRAWING.

EXPERIMENTAL NO:		DIVISION ASSIGNED: dataMate Division	
TOLERANCE UNLESS OTHERWISE SPECIFIED	MATERIAL: SEE NOTES	DRAWN BY: A. PIRILLIS	DATE: 10/02/07
METRIC ±	INCHES ±	CHECKED BY: J. NOWAK	10/02/07
FRACS: DEC: XX ± .010	XXX ± .005	ENGR. APPROVAL: B. SKEPNEK	10/02/07
ANGLES ±		APPROVED BY: A. CHIAPPETTA	10/02/07
TOOLING DWG <input type="checkbox"/>		PART NO. CLASSIFICATION:	
PART DWG <input type="checkbox"/>			
BREAK SHARP EDGES REMOVE ALL BURRS			
<small>THE INFORMATION DISCLOSED IN THIS DOCUMENT IS PROPRIETARY TO METHODE ELECTRONICS, INC. AND MAY NOT BE USED FOR MANUFACTURE OR ANY OTHER PURPOSE WITHOUT THE WRITTEN CONSENT OF METHODE. DRILLS SUBJECT TO CHANGE AS THEY MAY CHANGE WITH RESPECT TO PRODUCT IMPROVEMENT.</small>		TITLE: 2 PORT SFP+ EMI CAGE (IMPROVED) WITH GASKET PRESS FIT PINS	SIZE: C
		CODE IDENT.:	DWG. NUMBER: DM7050-2-X
			Rev.:
		SCALE:	SHI. 2 OF 2

