

REVISION AND CHANGE EFFECTIVITY DATE				
LTR.	ECN	DESCRIPTION	DATE	APP'D.
A	EC1349	CHANGED MATERIAL; INCREASED PIN WIDTH (7X);	05/21/02	AP
—	—	ADDED LOGO; ADDED TWO SOLDER LOCATIONS	—	—
B	EC1368	CHANGED PLATING	10/18/02	TL
C	EC1449	ADDED PLATING OPTIONS	03/09/05	AP
D	EC1508	ADDED TAB DIMENSIONS	08/30/06	AP
E	EC1527	ALL PLATING OPTIONS RoHS, INC. TOL. ON PCB ADDED OPERATING AND STORAGE TEMP NOTE	03/23/07	AP

NOTES:

- 1) MATERIAL: 0.010" THICK BRASS ALLOY; C2680 - FULL HARD
- 2) PLATING: SEE PLATING OPTIONS
- 3) CAGE IS SOLDERED AT SEAM AT 5 LOCATIONS
- 4) CAGE IS STAMPED 'R' FOR RoHS COMPLIANCE
- 5) OPERATING AND STORAGE TEMPERATURE: -40°C TO +160°C

ORDERING INFO:

DM7034-X

SERIES

PLATING OPTIONS:

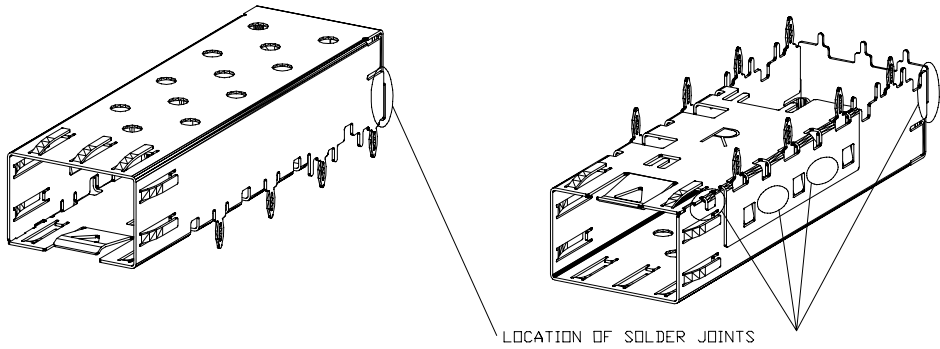
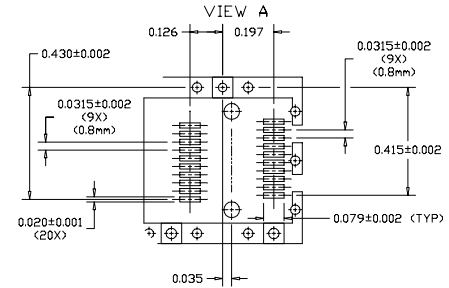
- R FOR 100μIN MATTE TIN OVER 50μIN NICKEL  
RoHS COMPLIANT
- G FOR FLASH GOLD OVER 50μIN NICKEL  
RoHS COMPLIANT
- N FOR 100μIN NICKEL  
RoHS COMPLIANT

DO NOT SCALE DRAWING.

EXPERIMENTAL NO:		DIVISION ASSIGNED: dataMate Division	
TOLERANCE UNLESS OTHERWISE SPECIFIED	MATERIAL: SEE NOTES	DRAWN BY: A. PIRILLIS	DATE: 02/01/02
METRIC ±	INCHES ±	CHECKED BY: T. LEGITTIMO	DATE: 02/01/02
FRACTIONS ±	DEC. XX ± .010	ENGR. APPROVAL: BOB SKEPNEK	DATE: 02/01/02
XXX ± .005	ANGLES ±	APPROVED BY: JOHN DALY	DATE: 02/01/02
TOOLING DWG □	PART DWG □	PART NO. CLASSIFICATION:	
BREAK SHARP EDGES	REMOVE ALL BURRS		
<small>THE INFORMATION DISCLOSED IN THIS DOCUMENT IS PROPRIETARY TO METHODE ELECTRONICS, INC. AND MAY NOT BE USED FOR MANUFACTURE OR ANY OTHER PURPOSES WITHOUT THE WRITTEN CONSENT OF METHODE. DETAILS SUBJECT TO CHANGE AS THEY MAY CHANGE WITH RESPECT TO PRODUCT IMPROVEMENT.</small>		TITLE: SFP CAGE - ONE PIECE WITH PRESS FIT PINS	SIZE: C
		CODE IDENT.:	DWG. NUMBER: DM7034-X
			Rev. E
		SCALE:	SHT. 1 OF 2

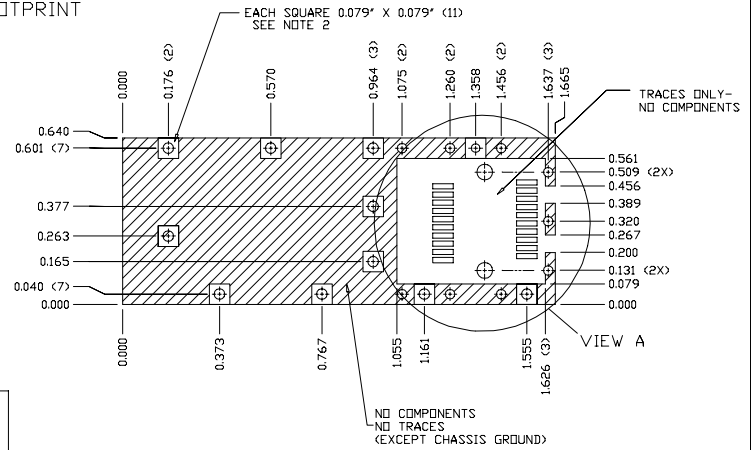
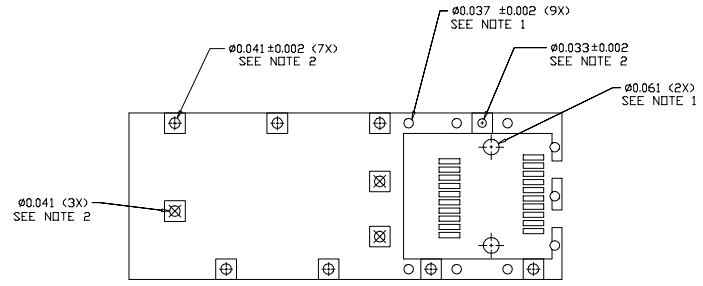


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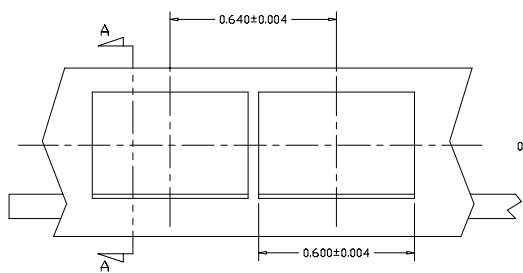


LOCATION OF SOLDER JOINTS

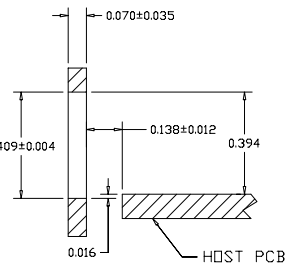
RECOMMENDED FOOTPRINT



RECOMMENDED PANEL CUTOUT



SECTION A-A



NOTES:

- 1) THRU HOLES, PLATING OPTIONAL
- 2) PADS AND VIAS ARE CHASSIS GROUND, 11 PLACES



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				PAGE: 2 OF 2	